



Koneru Lakshmaiah Education Foundation

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Ref: KLEF/AR/Ph.D/Engineering/2017-18/98

Date: 27-07-2018

NOTIFICATION

It is hereby notified that the Vice Chancellor, on the recommendations of the examiners appointed to adjudicate the Ph.D thesis entitled "*INHERENT AND EFFICIENT SOURCE - SIDE CAPACITOR VOLTAGE BALANCING TECHNIQUES FOR DIODE CLAMPED MLI*" submitted by **Mr.K. NARASIMHA RAJU**, Research Scholar, Koneru Lakshmaiah Education Foundation (Deemed to be University) with **Registered Number 13306019**, Department of Electrical and Electronics Engineering, under the supervision of **Dr. Obbu Chandrasekhar**, Professor, Department of Electrical and Electronics Engineering, K L E F for the award of Ph.D. Degree, and on consideration of the report submitted by the Examiners of the VIVA-VOCE Board, has ordered that **Mr. K. NARASIMHA RAJU** be declared qualified for the award of the **Degree of Doctor of Philosophy (Ph.D)** in "**Electrical and Electronics Engineering**" in the "**Faculty of Engineering**".

To

Mr. K. NARASIMHA RAJU,

Research Scholar,

Registered No. **13306019**,

Department of **Electrical and Electronics Engineering**

Koneru Lakshmaiah Education Foundation (Deemed to be University)




(Dr.T. Umamaheswara Rao)

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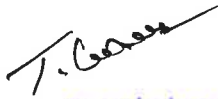
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